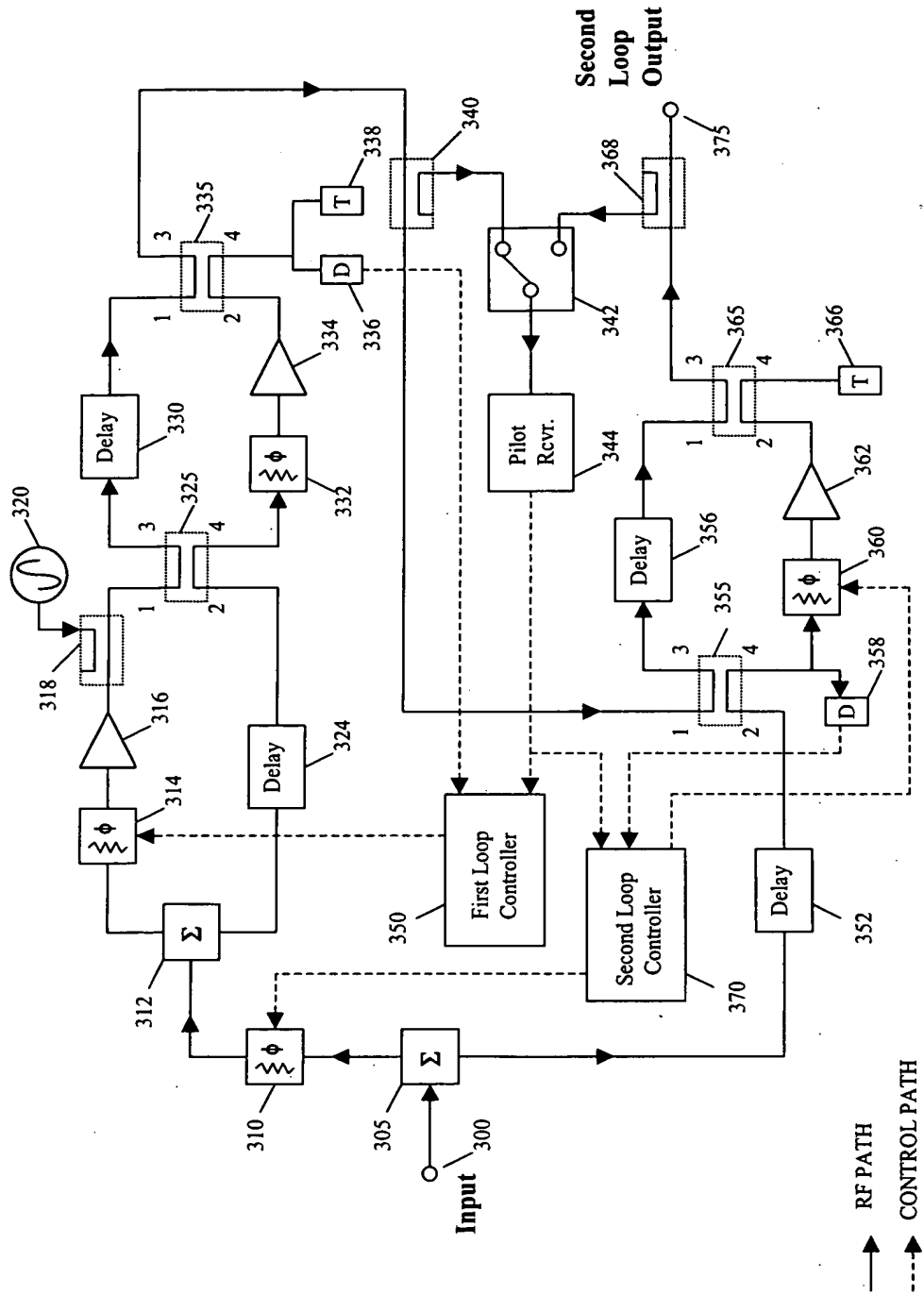


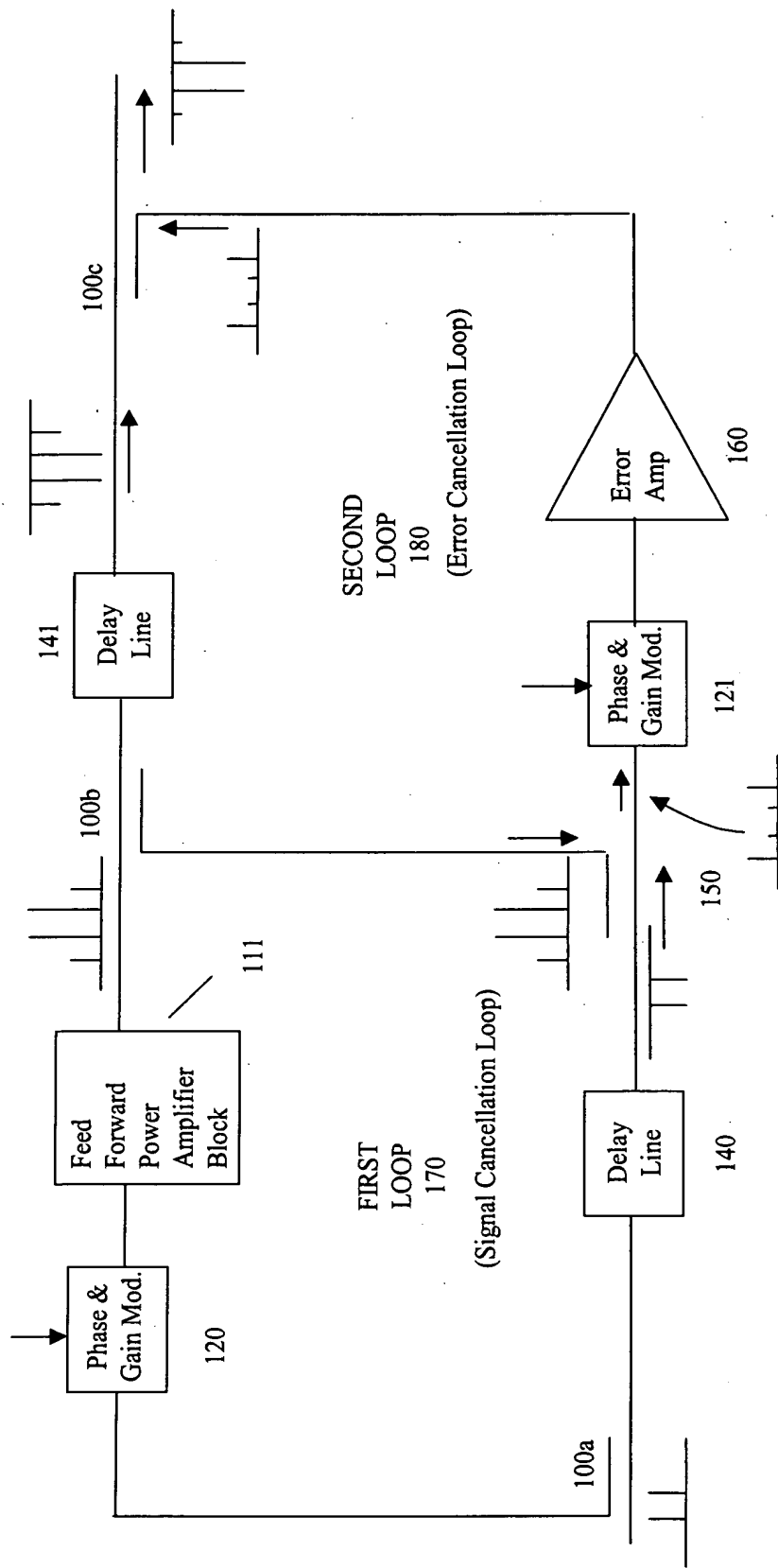
PRIOR ART

FIG. 1



PRIOR ART

FIG. 2



PRIOR ART

FIG. 3

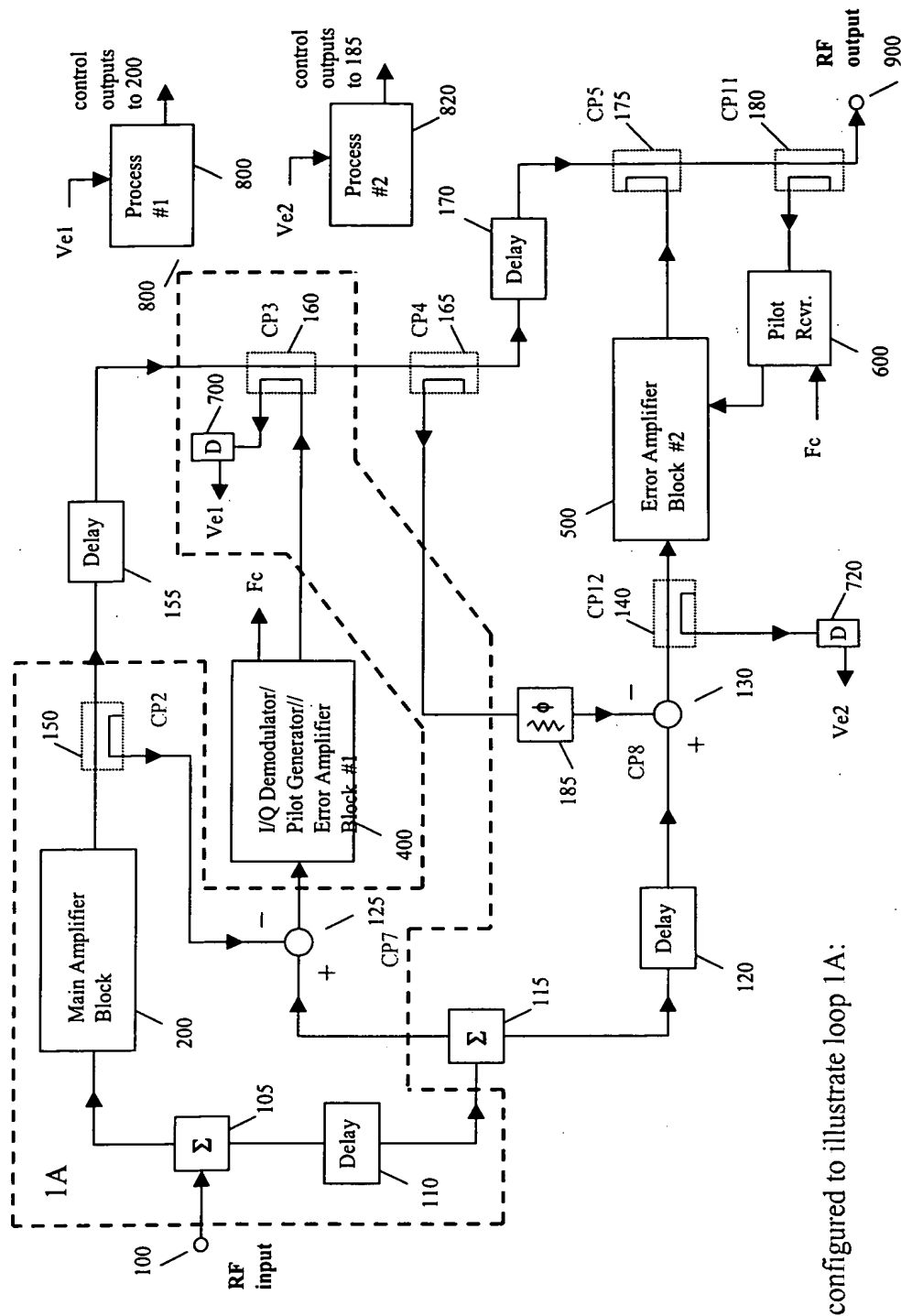


FIG. 4







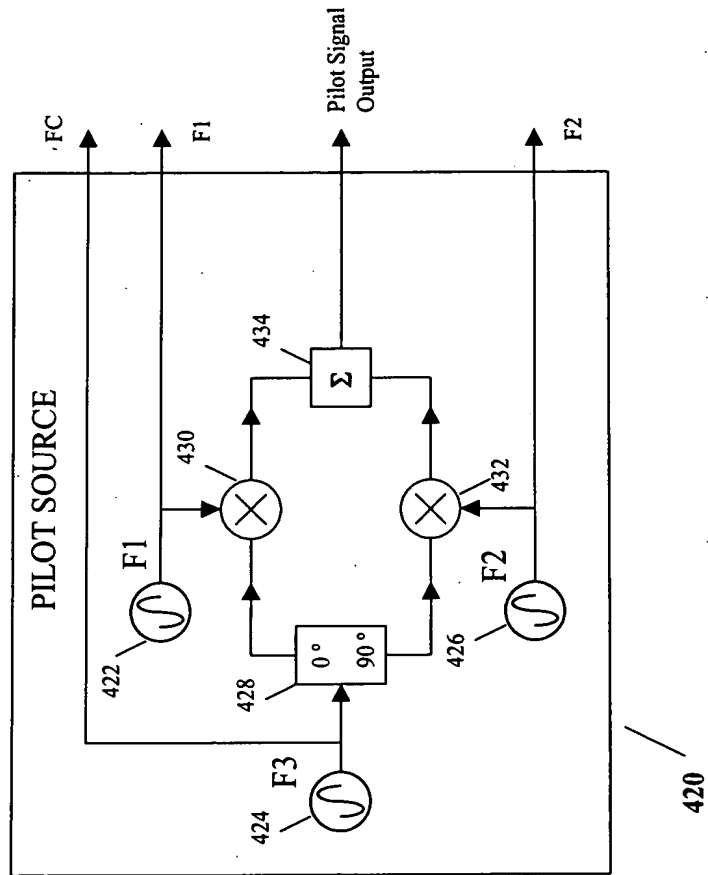


FIG. 9

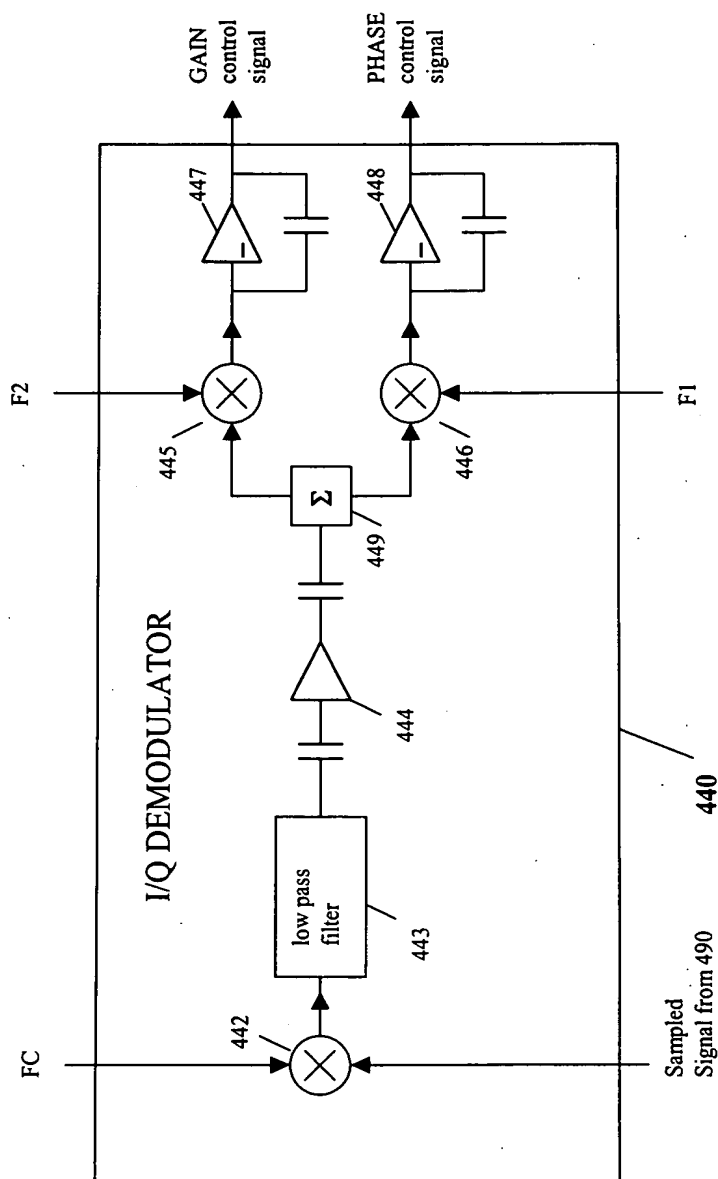


FIG. 10

Configured to illustrate output coupler loss minimization loop only:

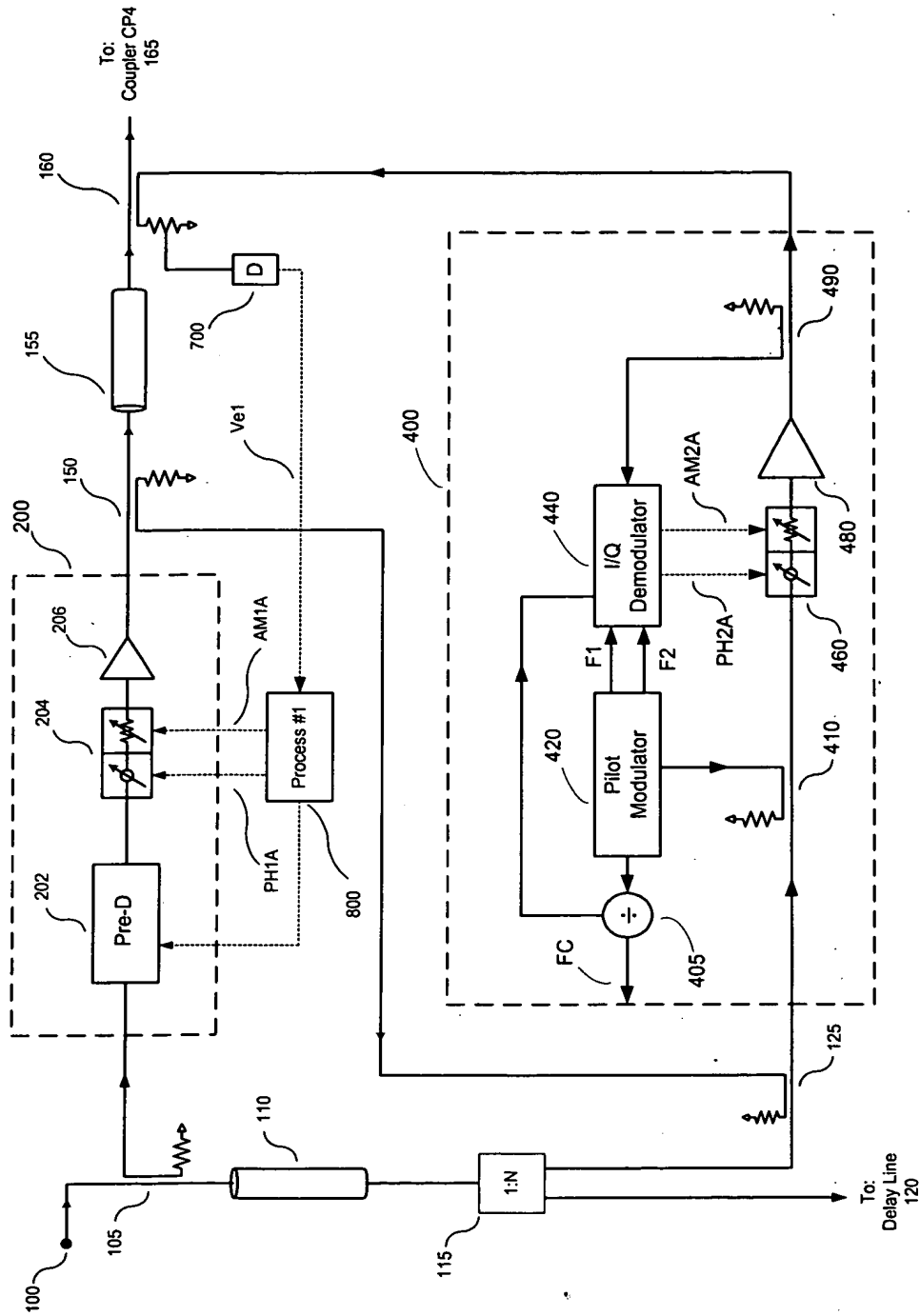


FIG. 11

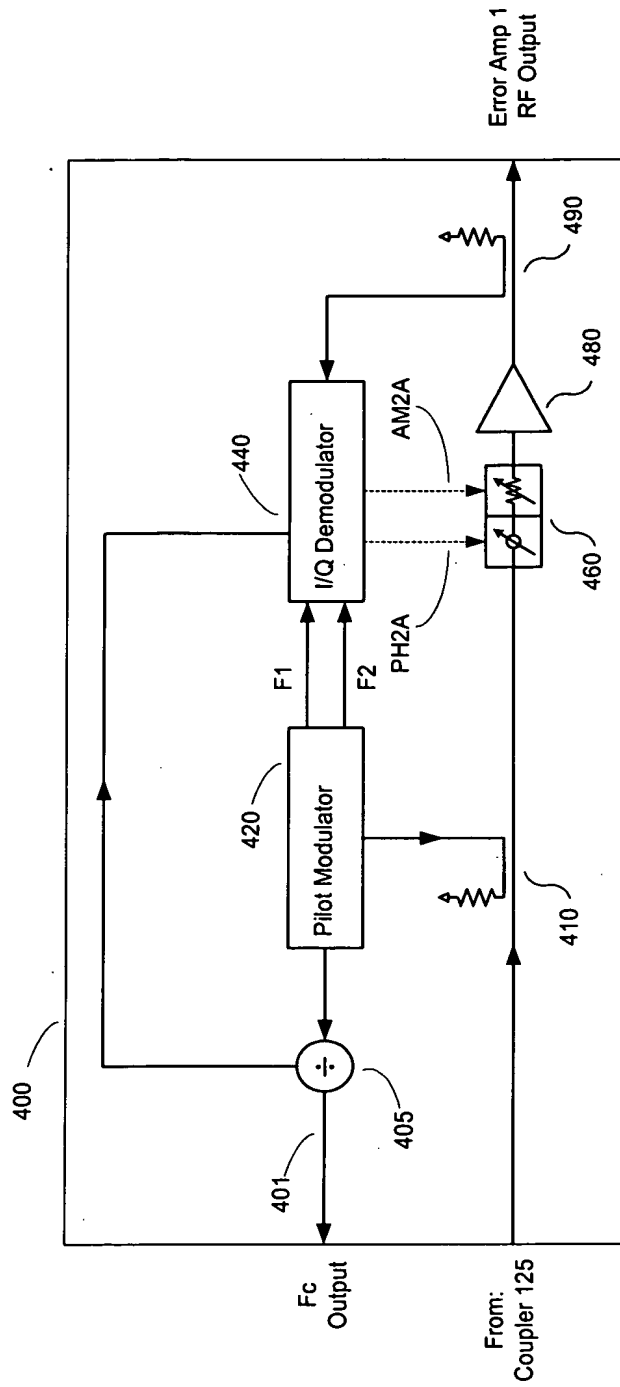


FIG. 12

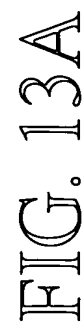


FIG. 13A.

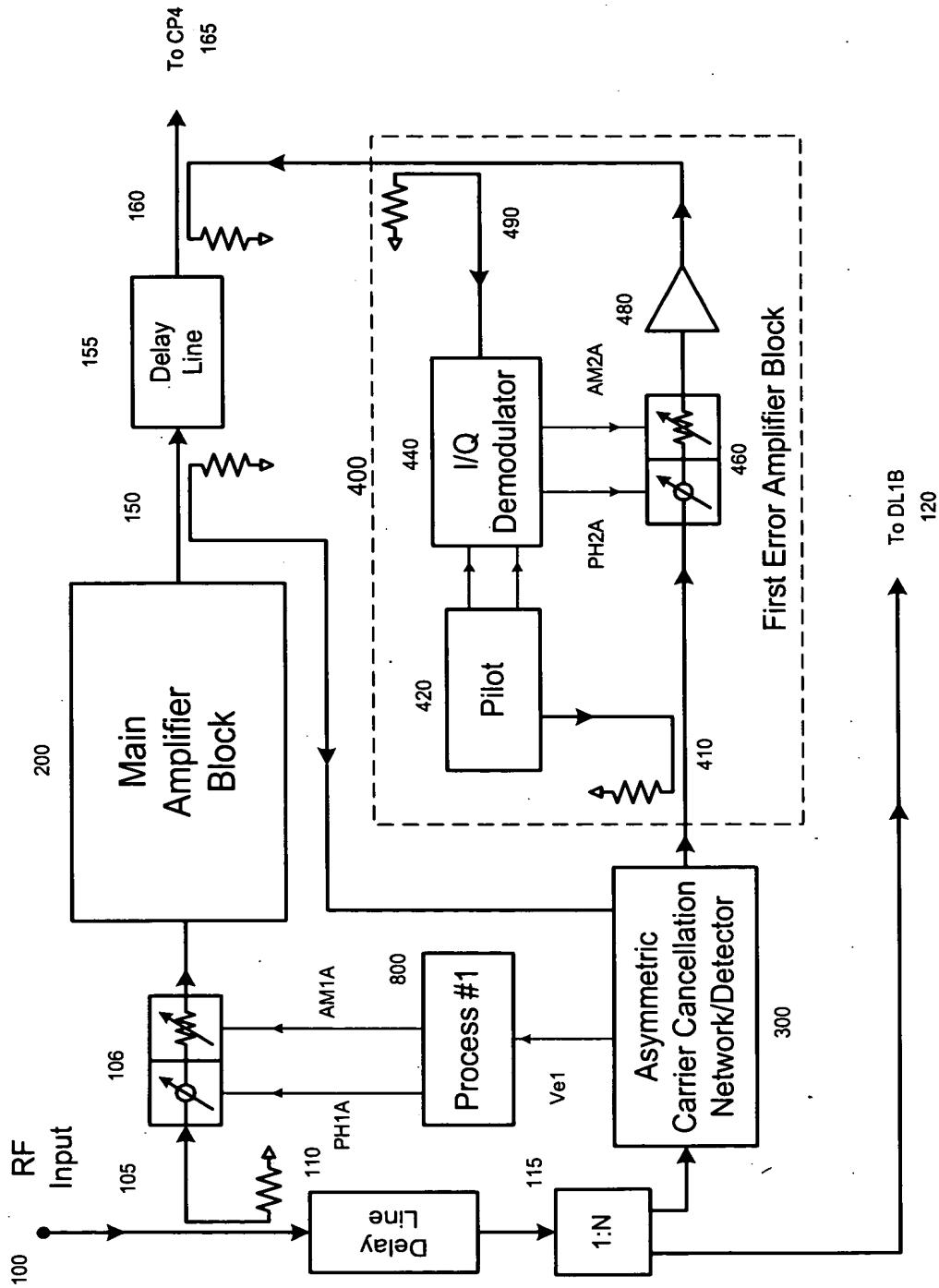


FIG. 13B

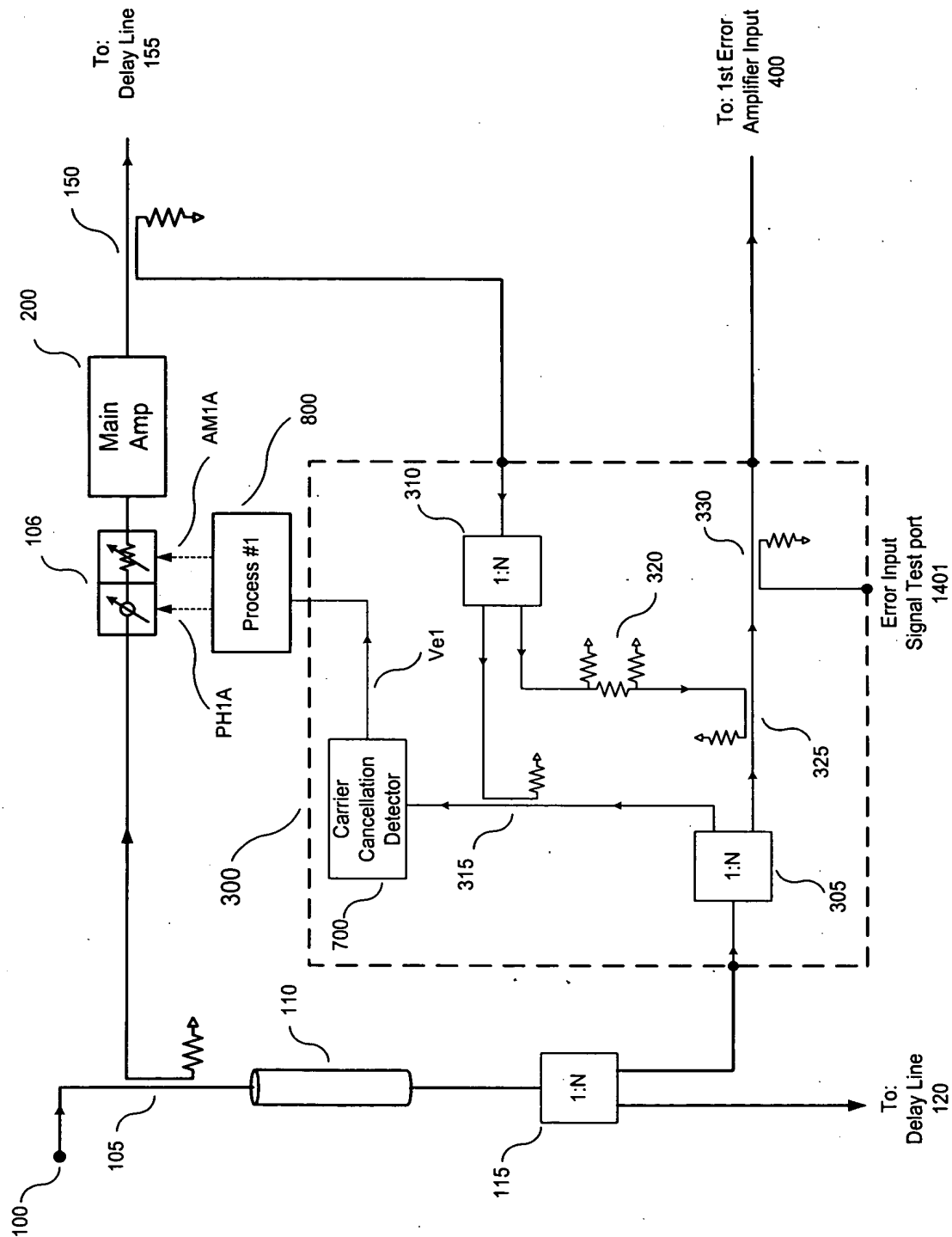


FIG. 14



Error Amplifier Stabilization

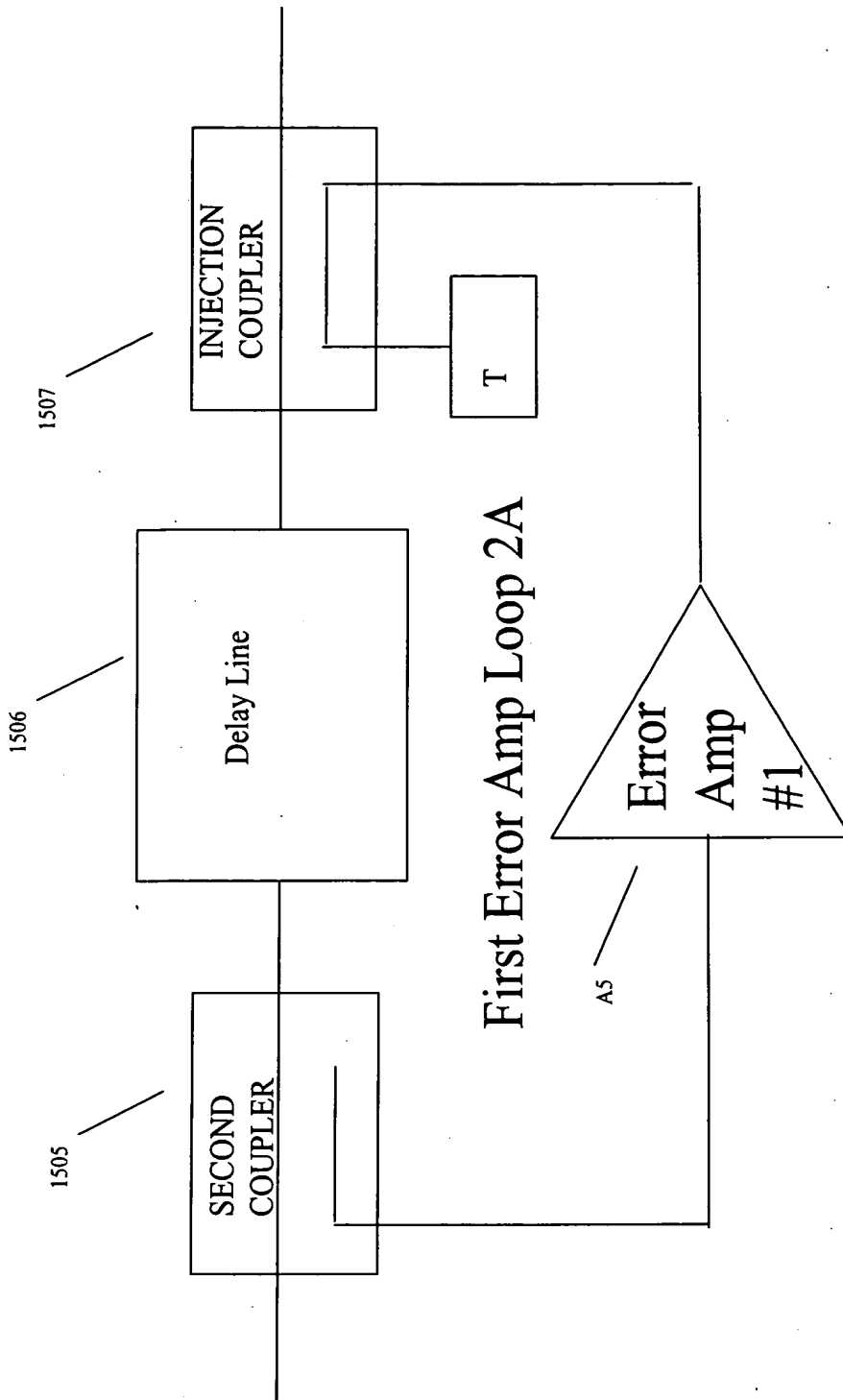


FIG. 16

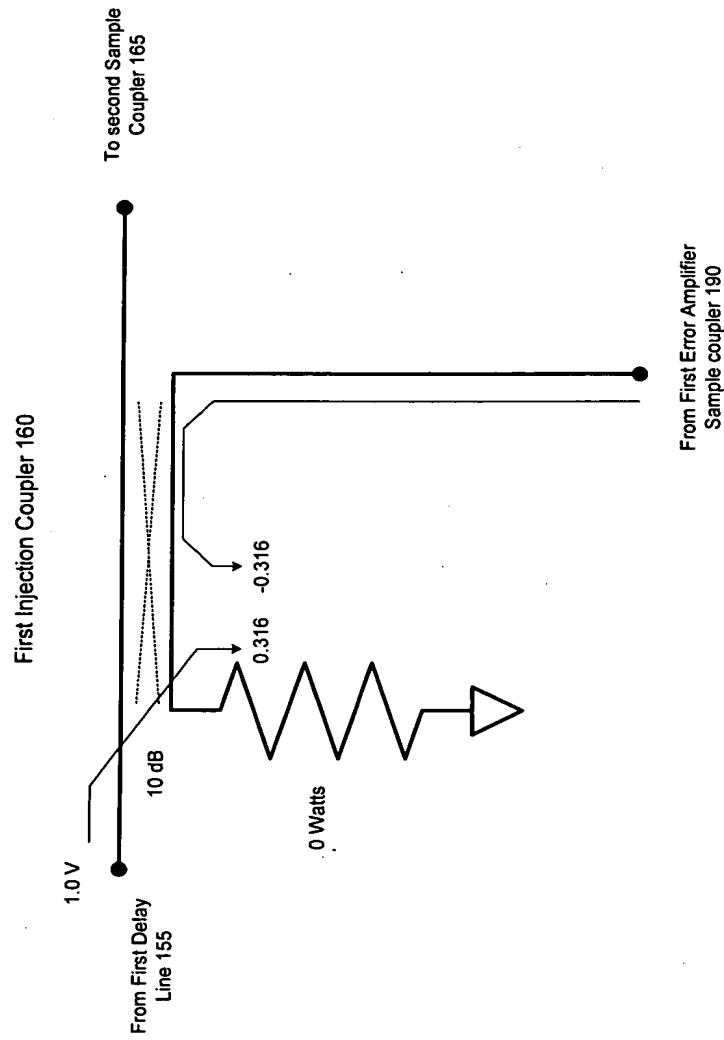


FIG. 17

Carrier Re-Injection

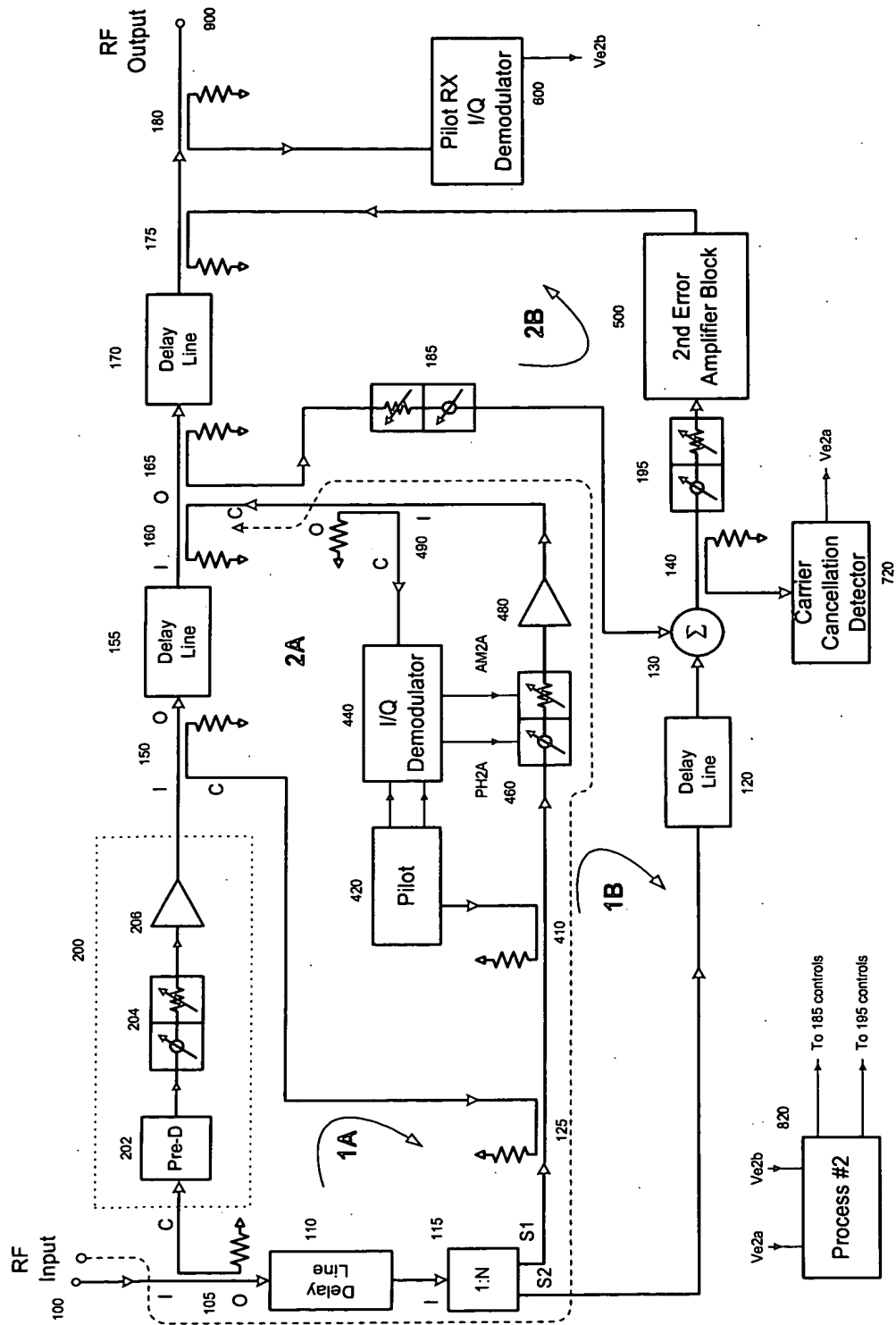


FIG. 18

Non-Overlapping Loop Adjustment

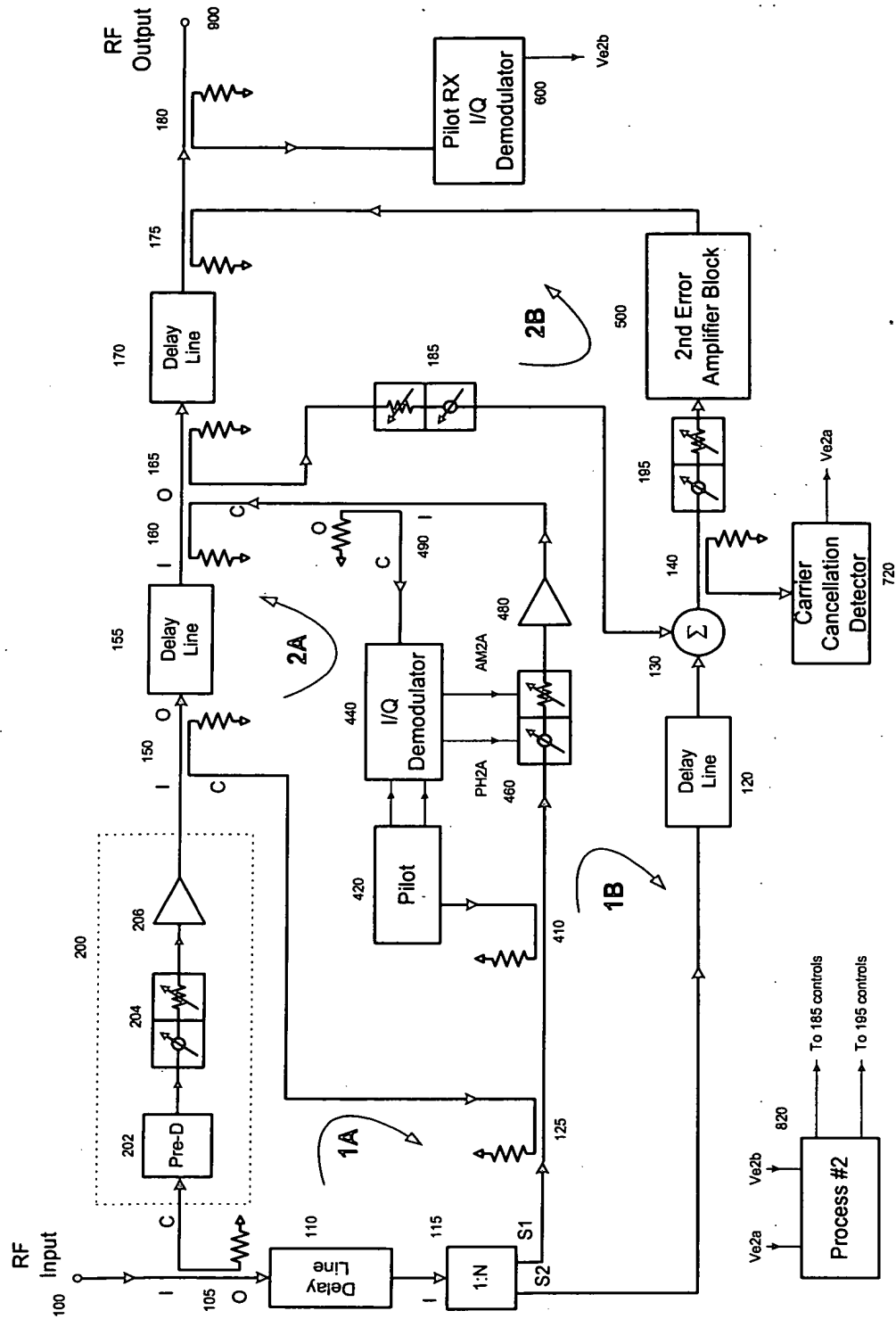


FIG. 19